

ABSTRACT OF THE DISCLOSURE



## Model Creation

Selected Input Profile configurations (input training data)

Generate Output Data

Input data and calculated or measured output data

Figure 1

## Model Application

Experimental Model design

Probability distribution on models

Model generation

Converged Models

Model application

Decomposition into profiles

Predicted characteristics

Profile parameters

Semiconductor Layout



Figure 2

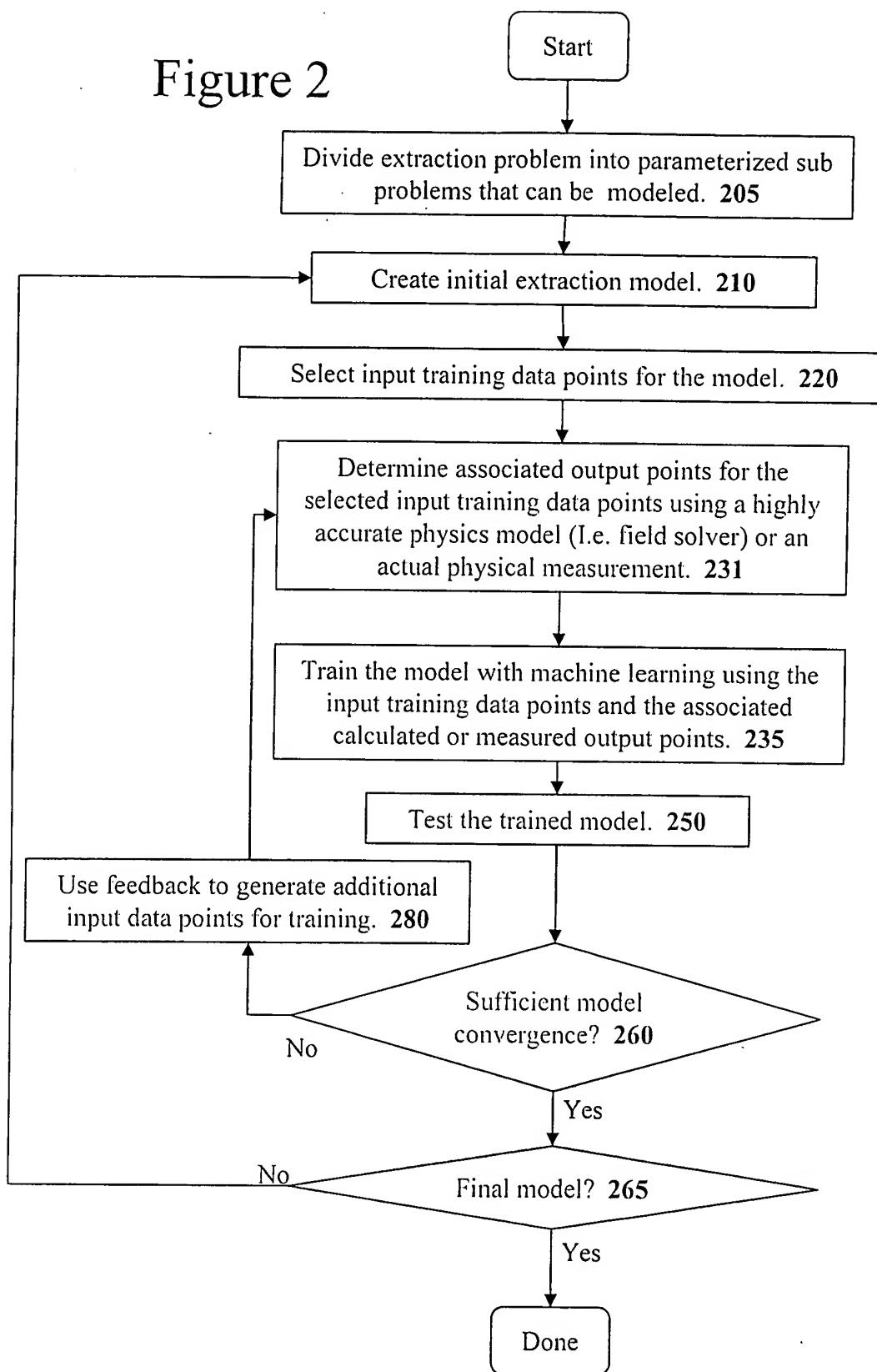
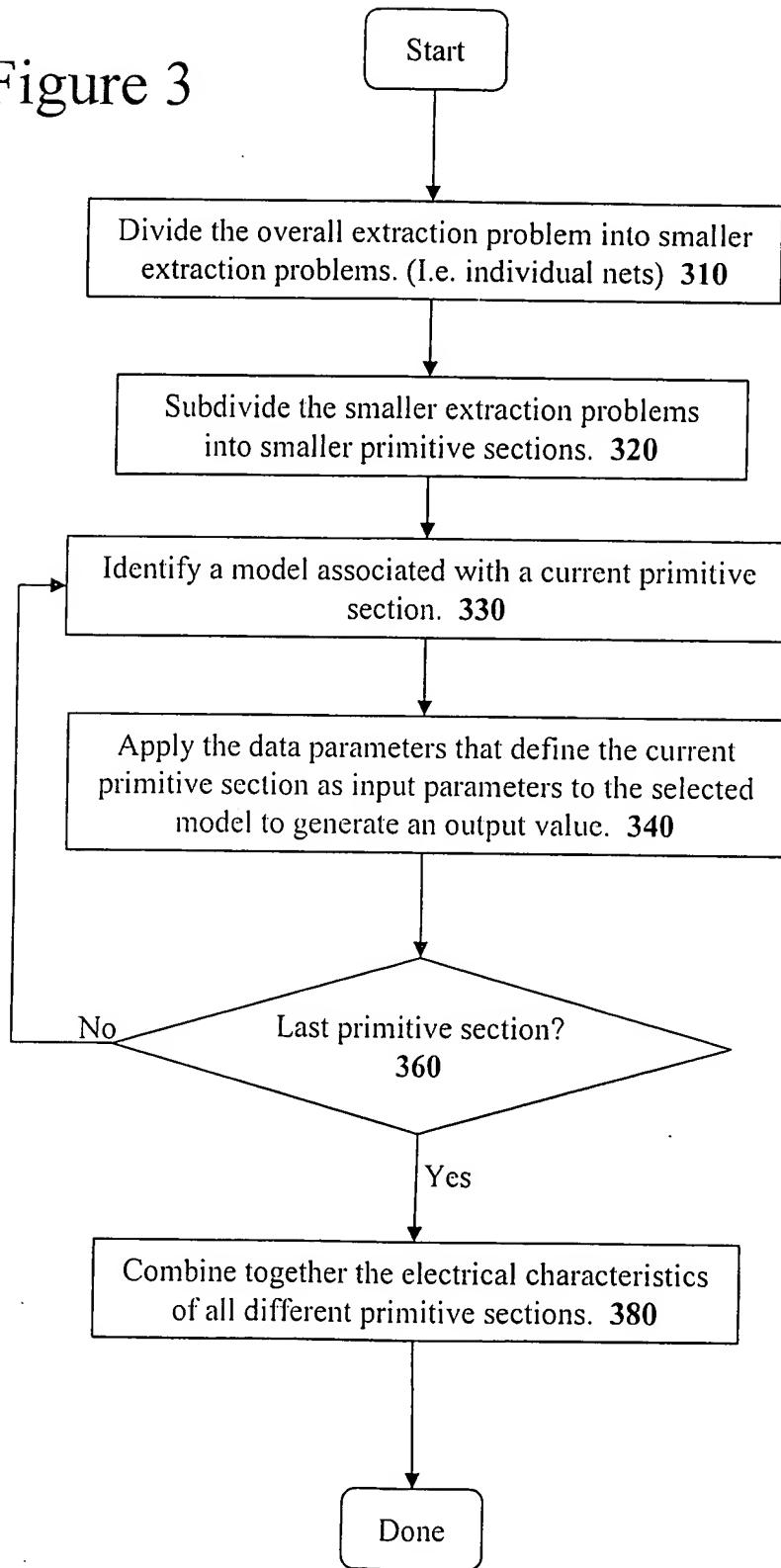




Figure 3



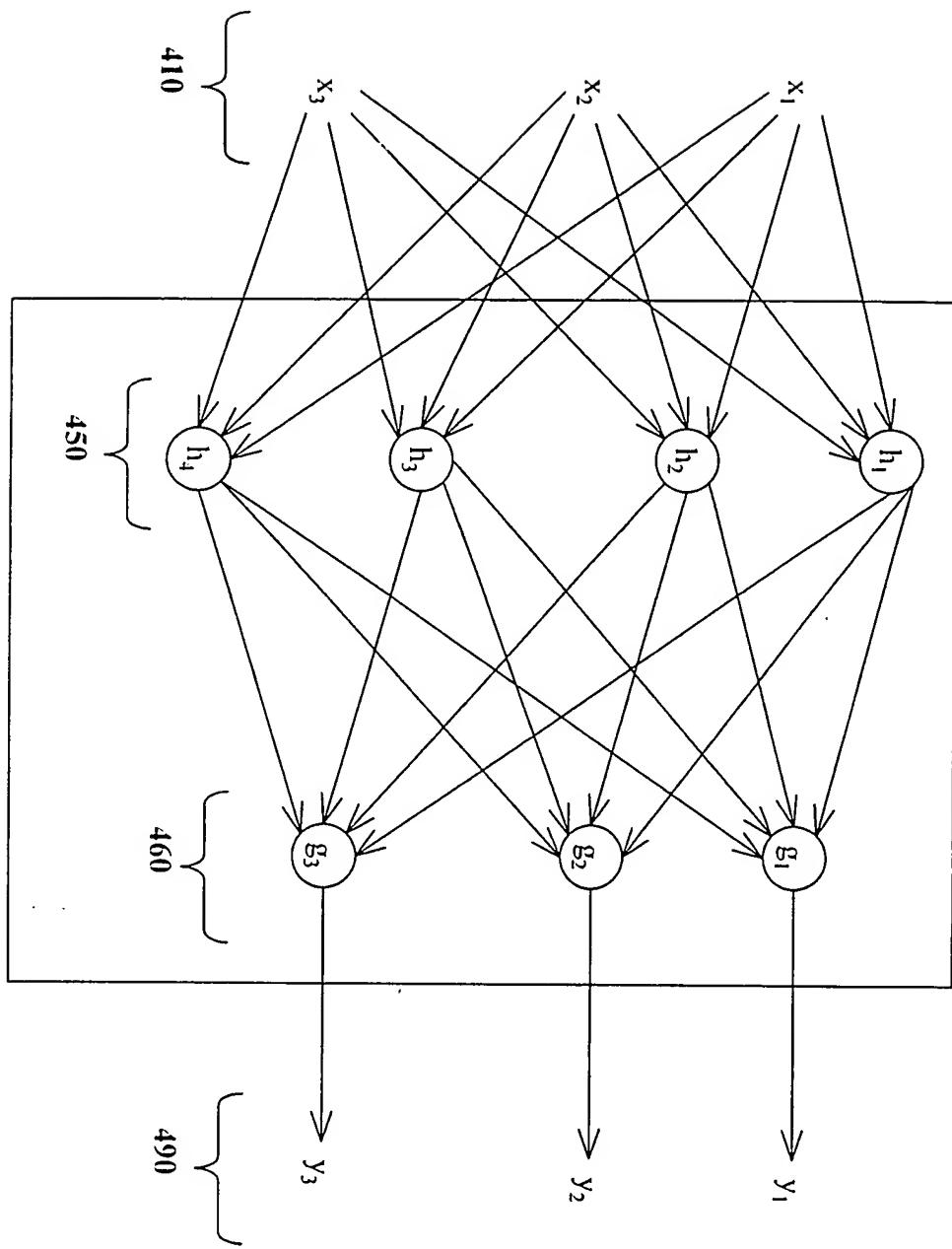
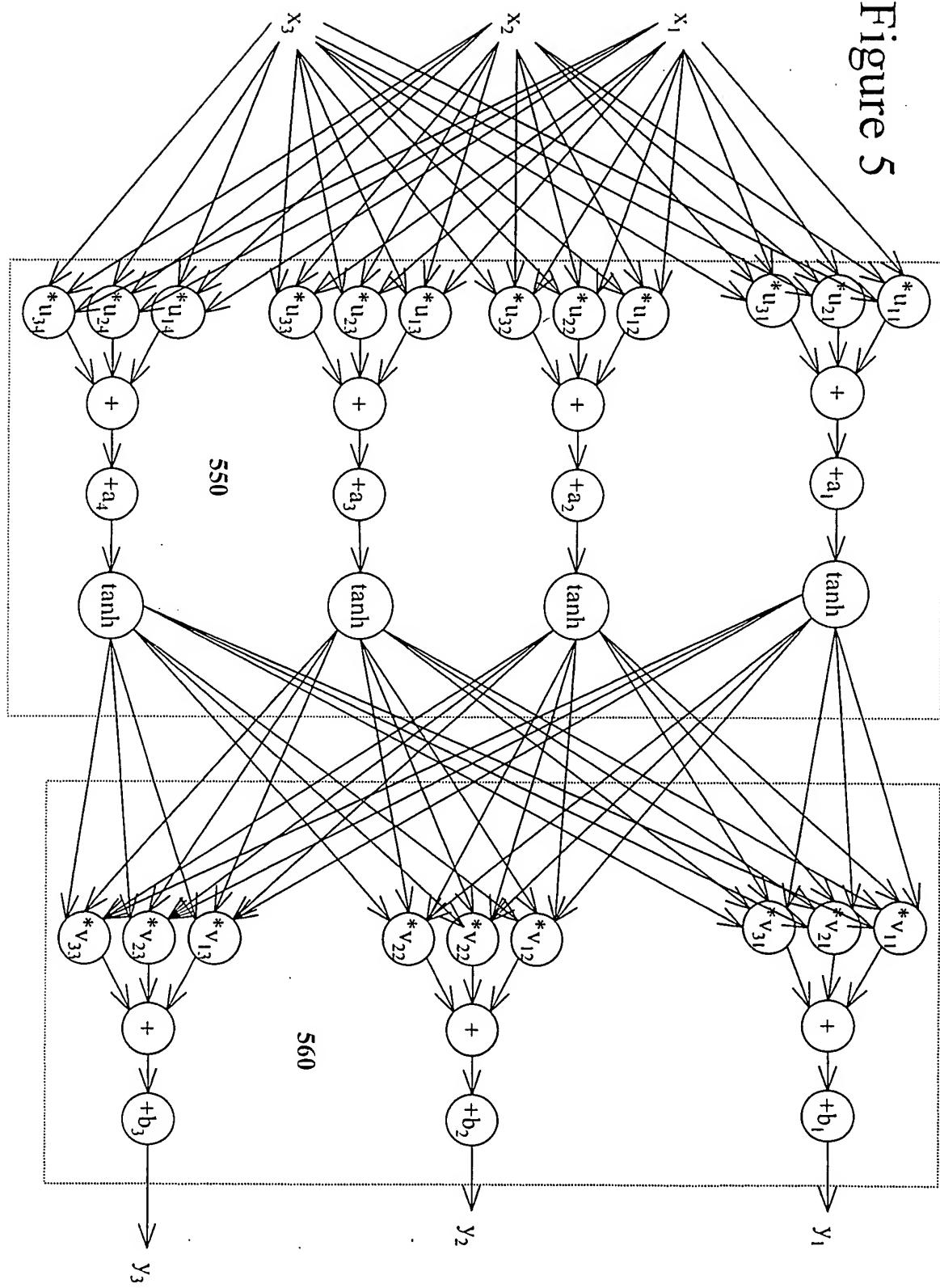


Figure 4



Figure 5



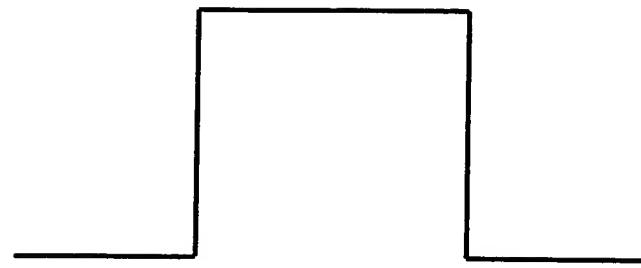


Figure 6a

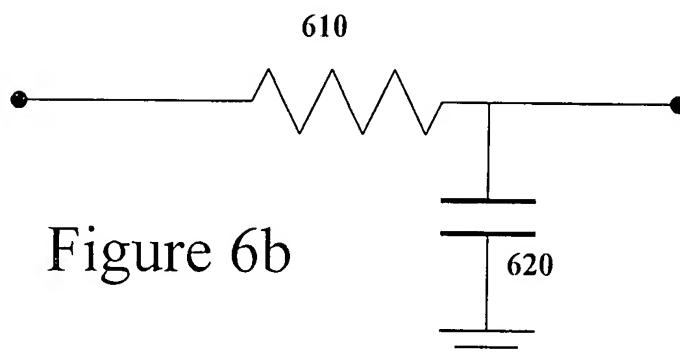


Figure 6b

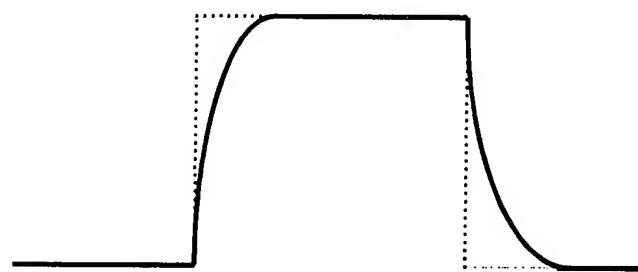


Figure 6c

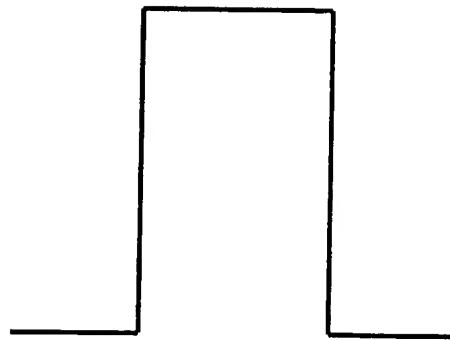


Figure 7a

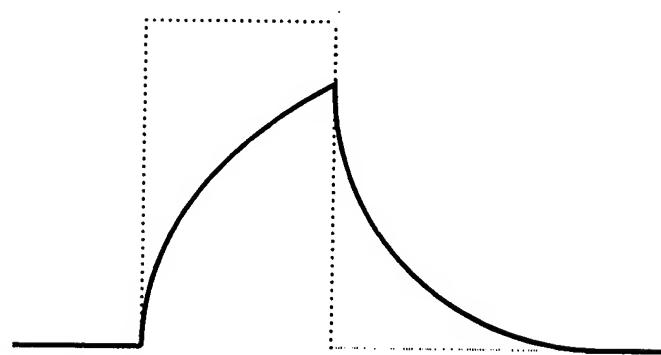


Figure 7b

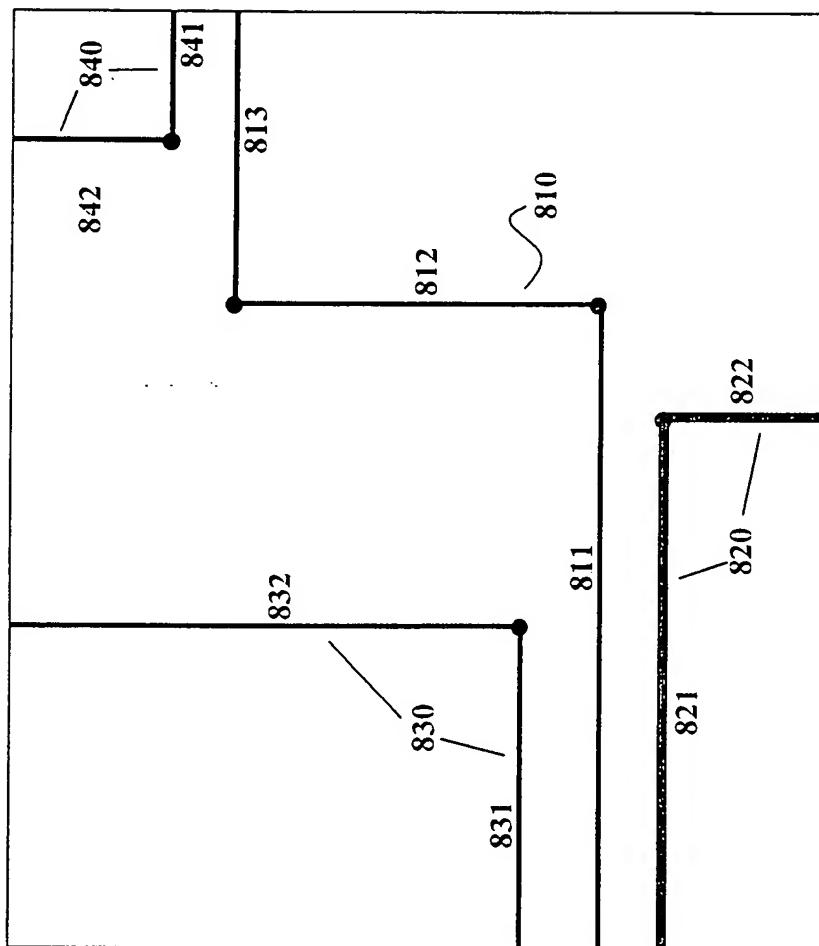


Figure 8A

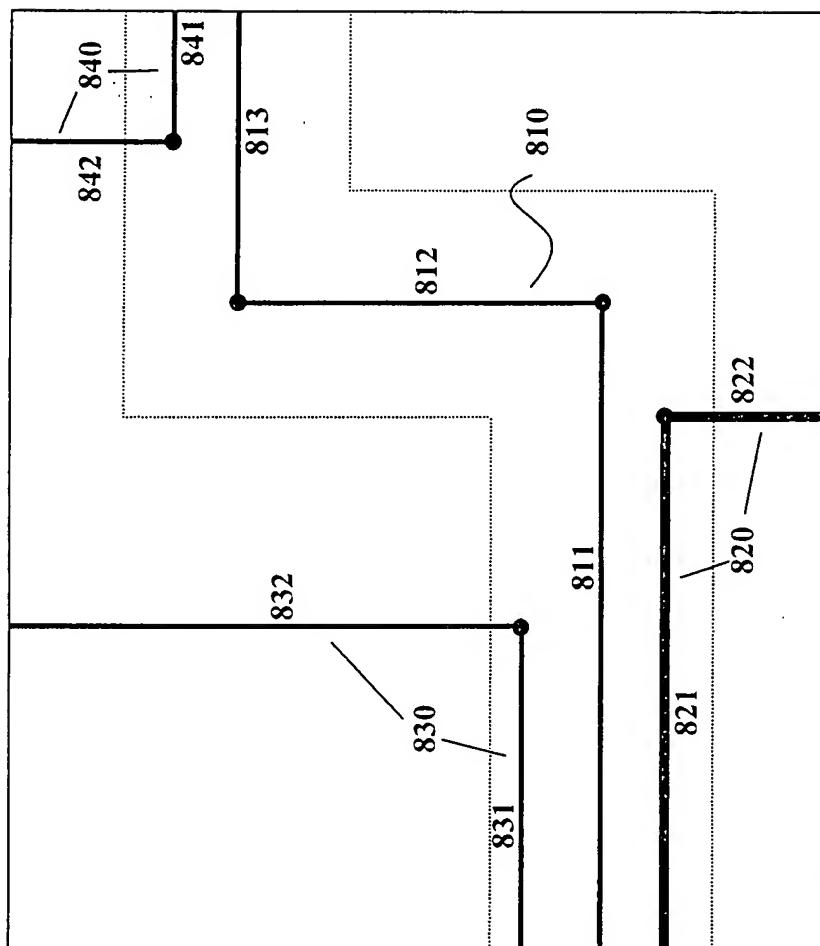


Figure 8B

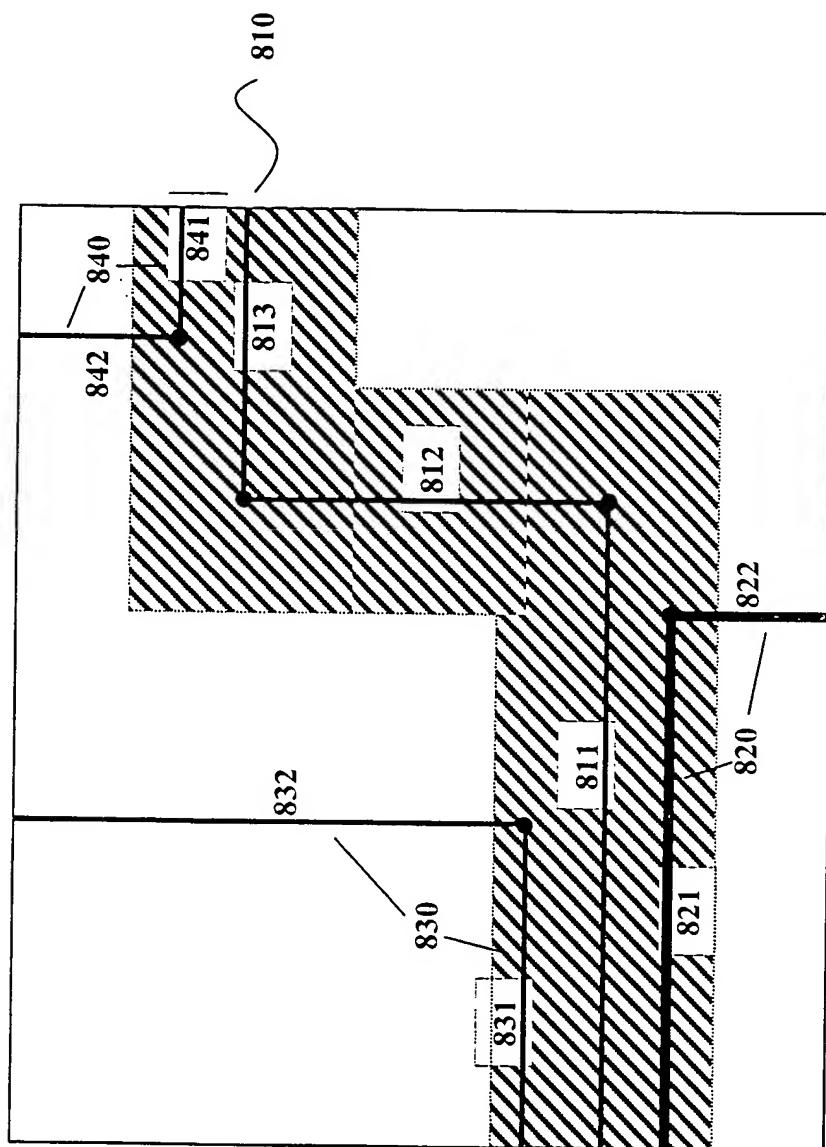


Figure 8C



Figure 8D

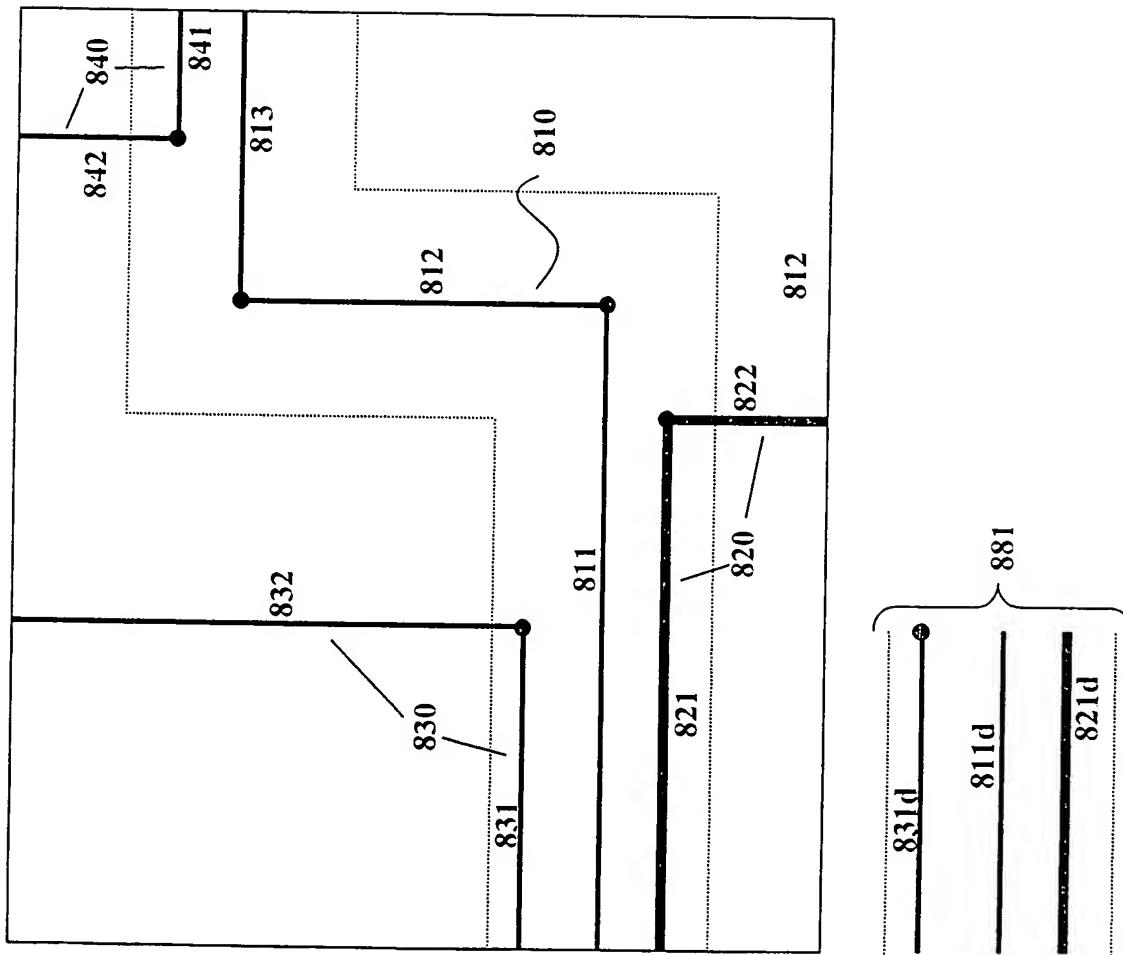




Figure 8E

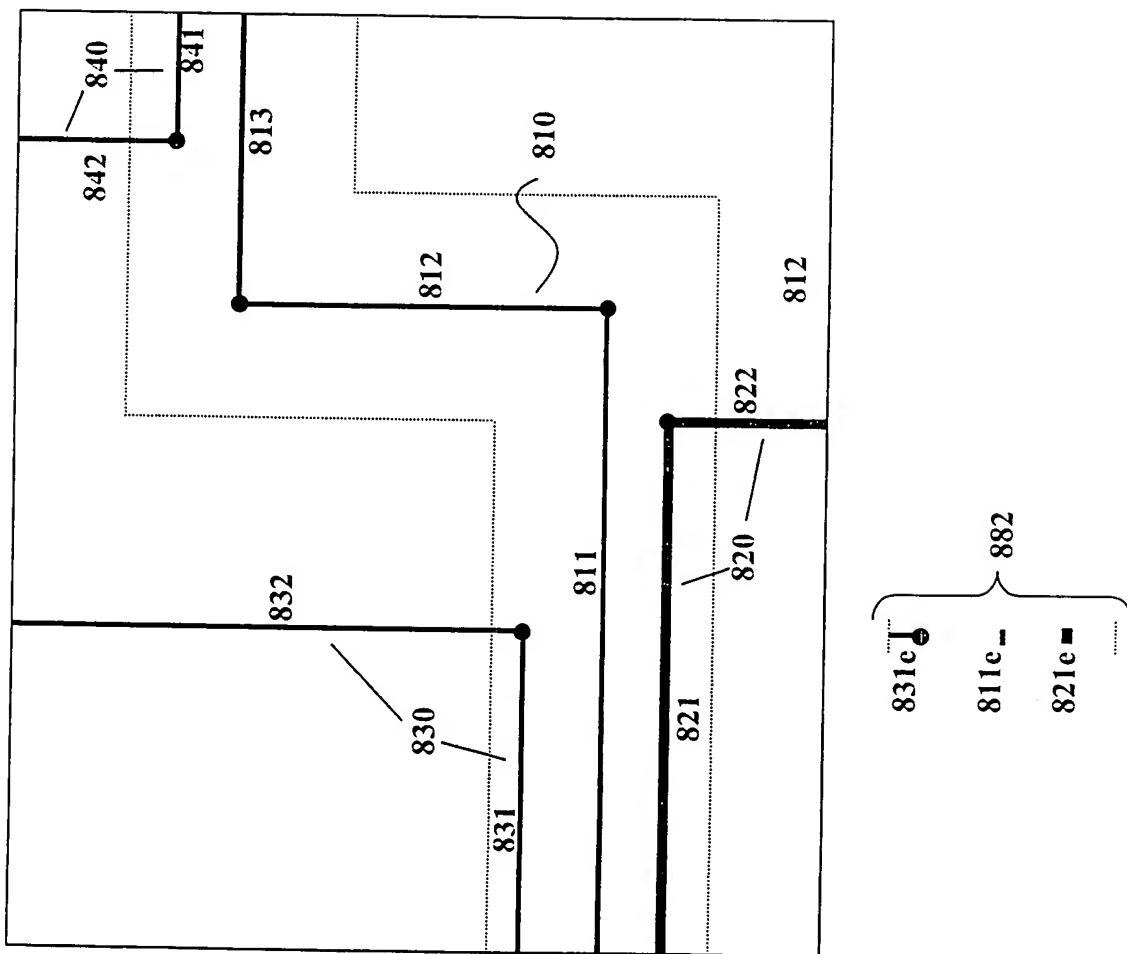




Figure 8F

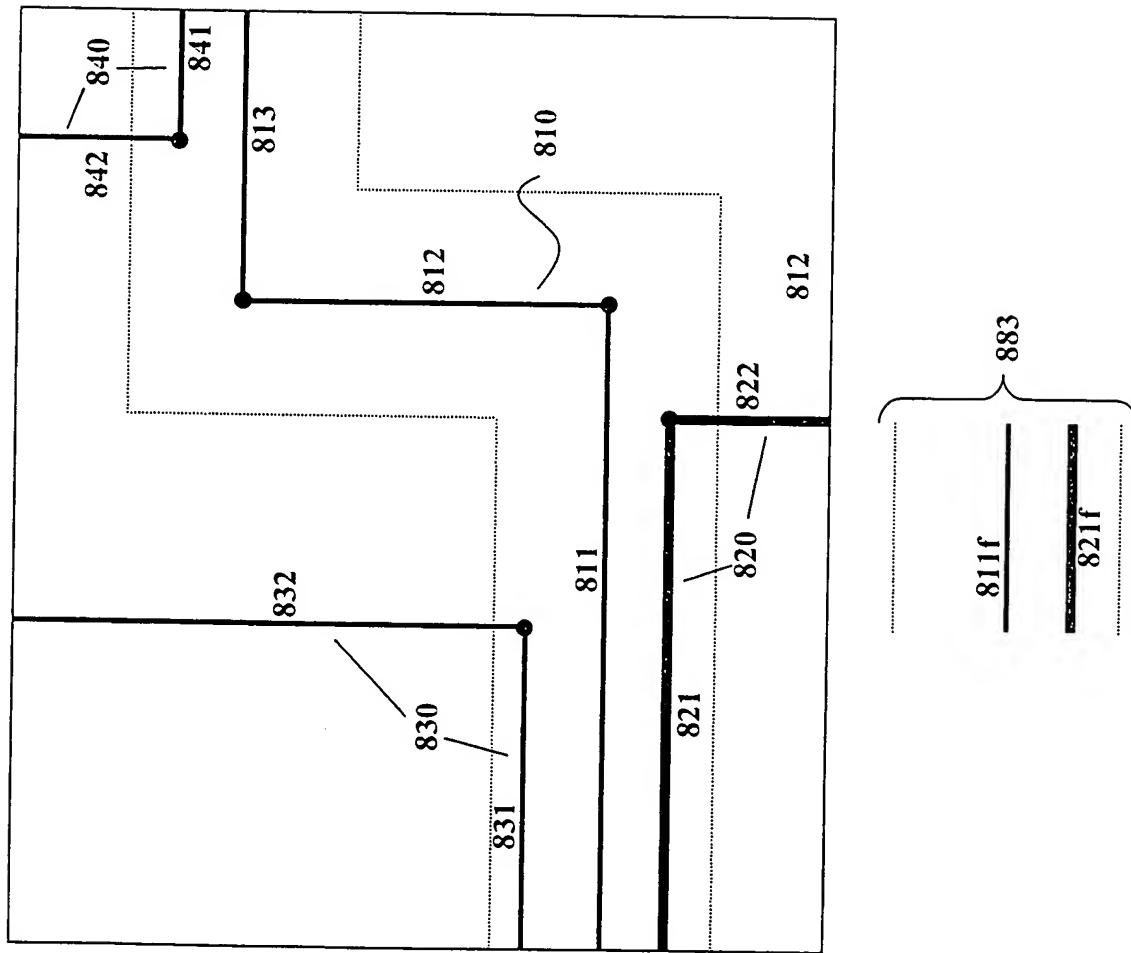




Figure 8G

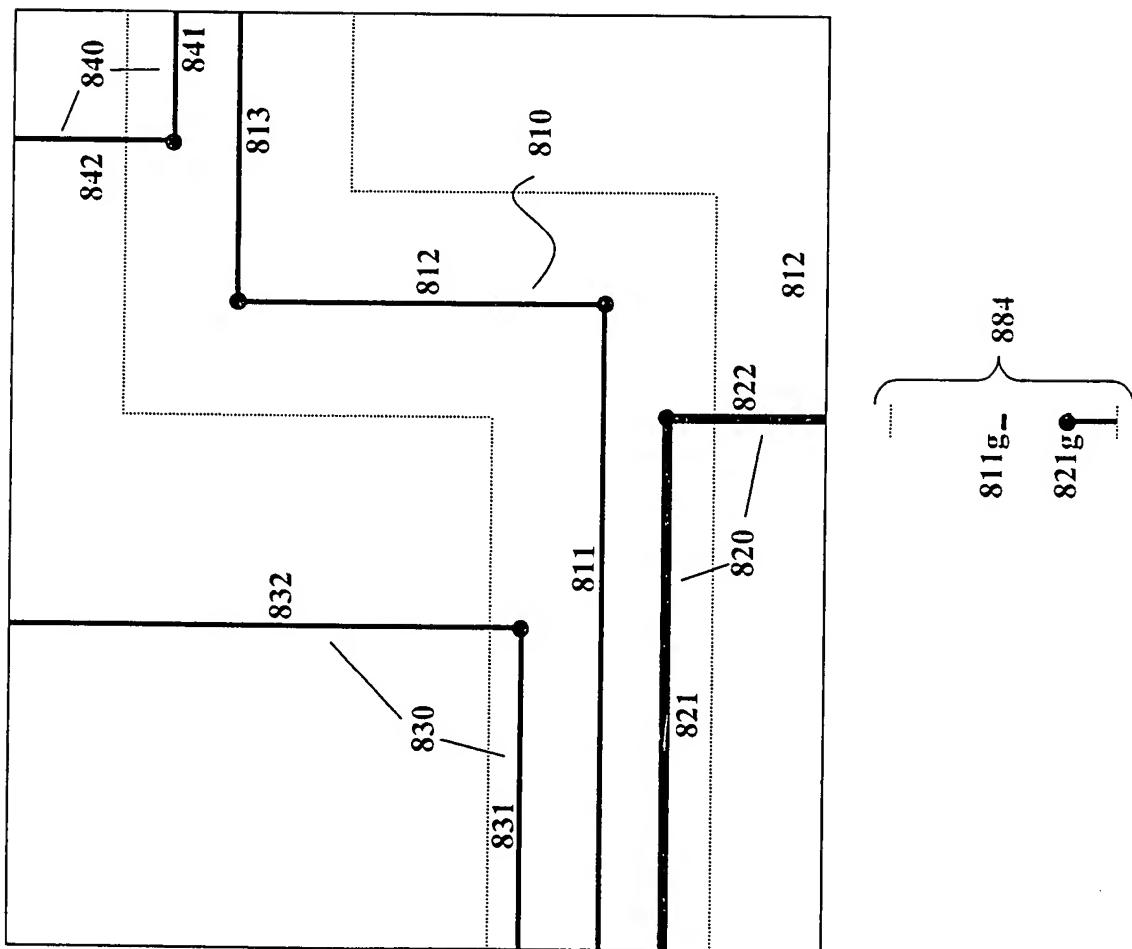




Figure 8H

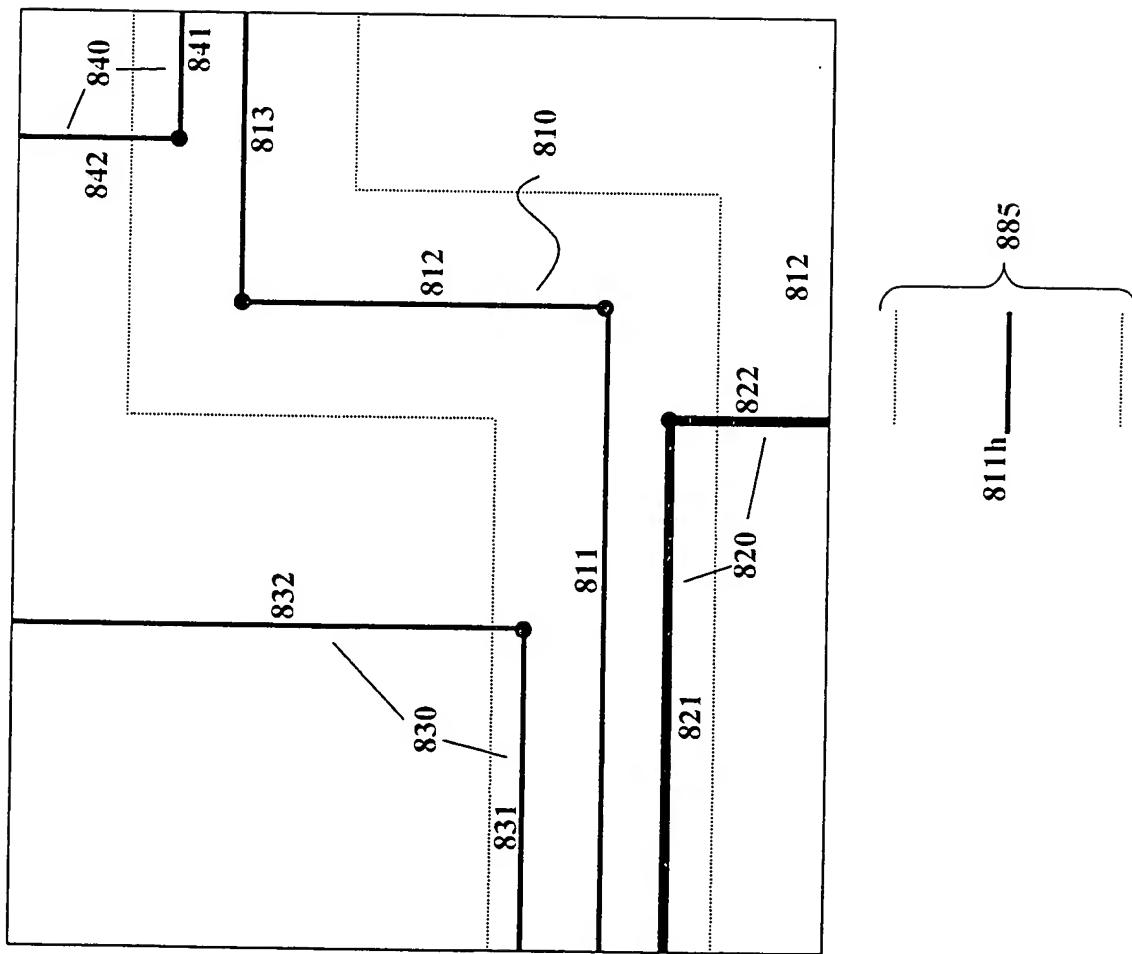




Figure 9A

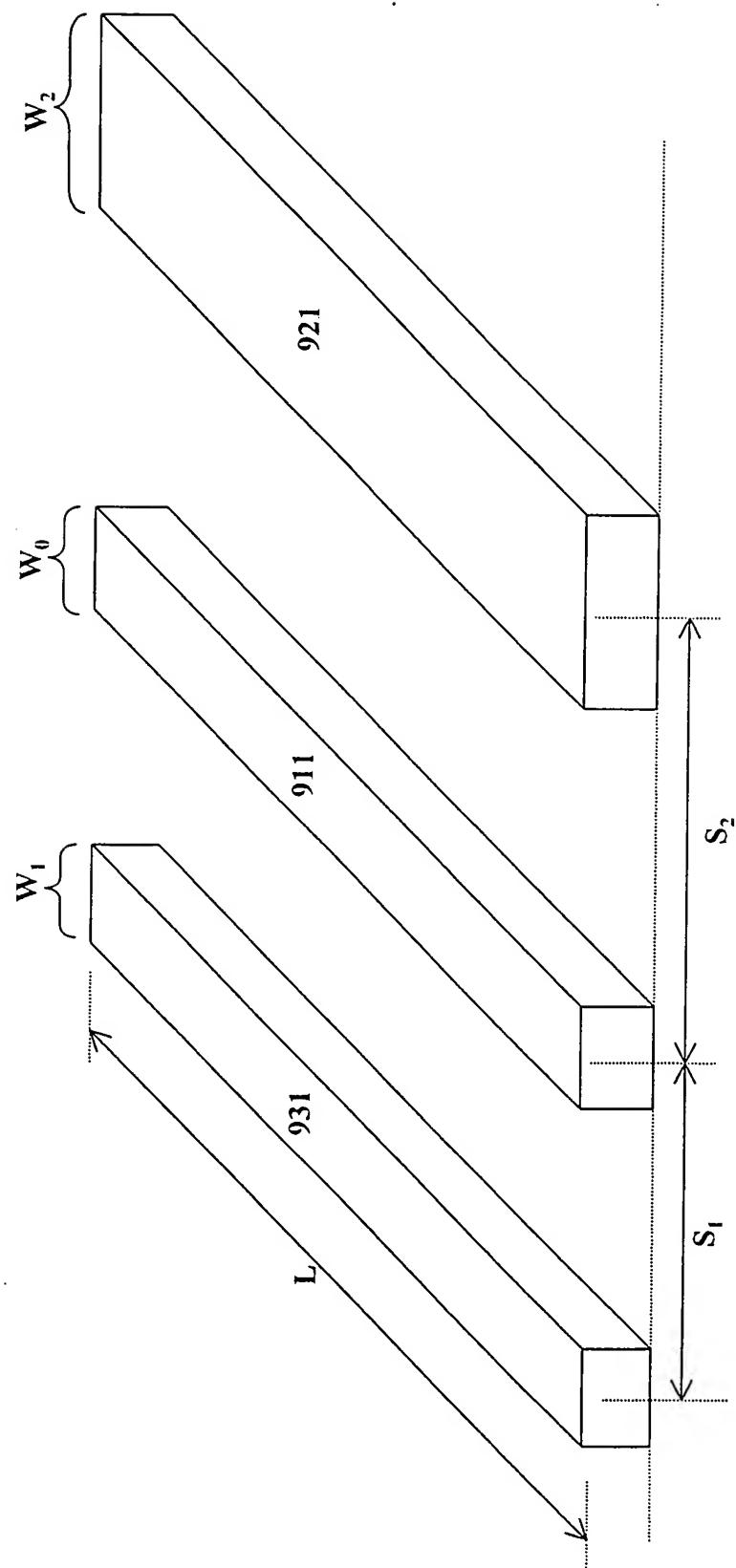
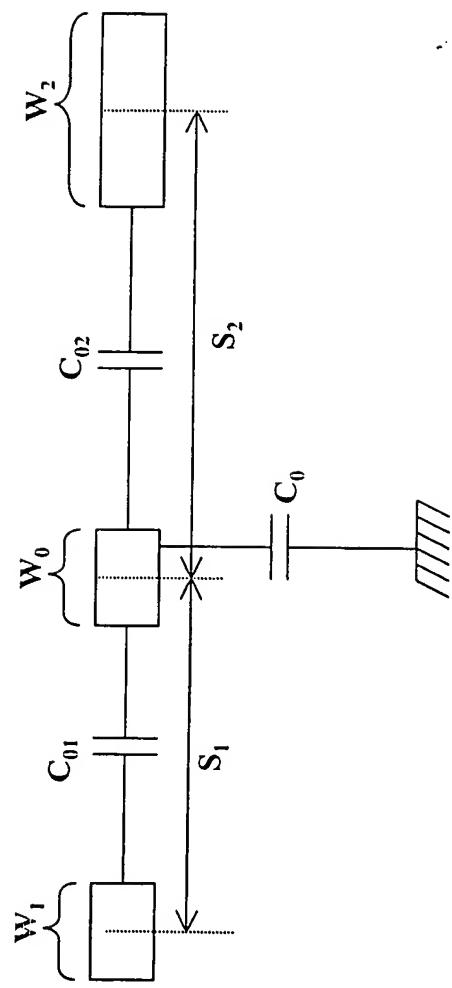




Figure 9B



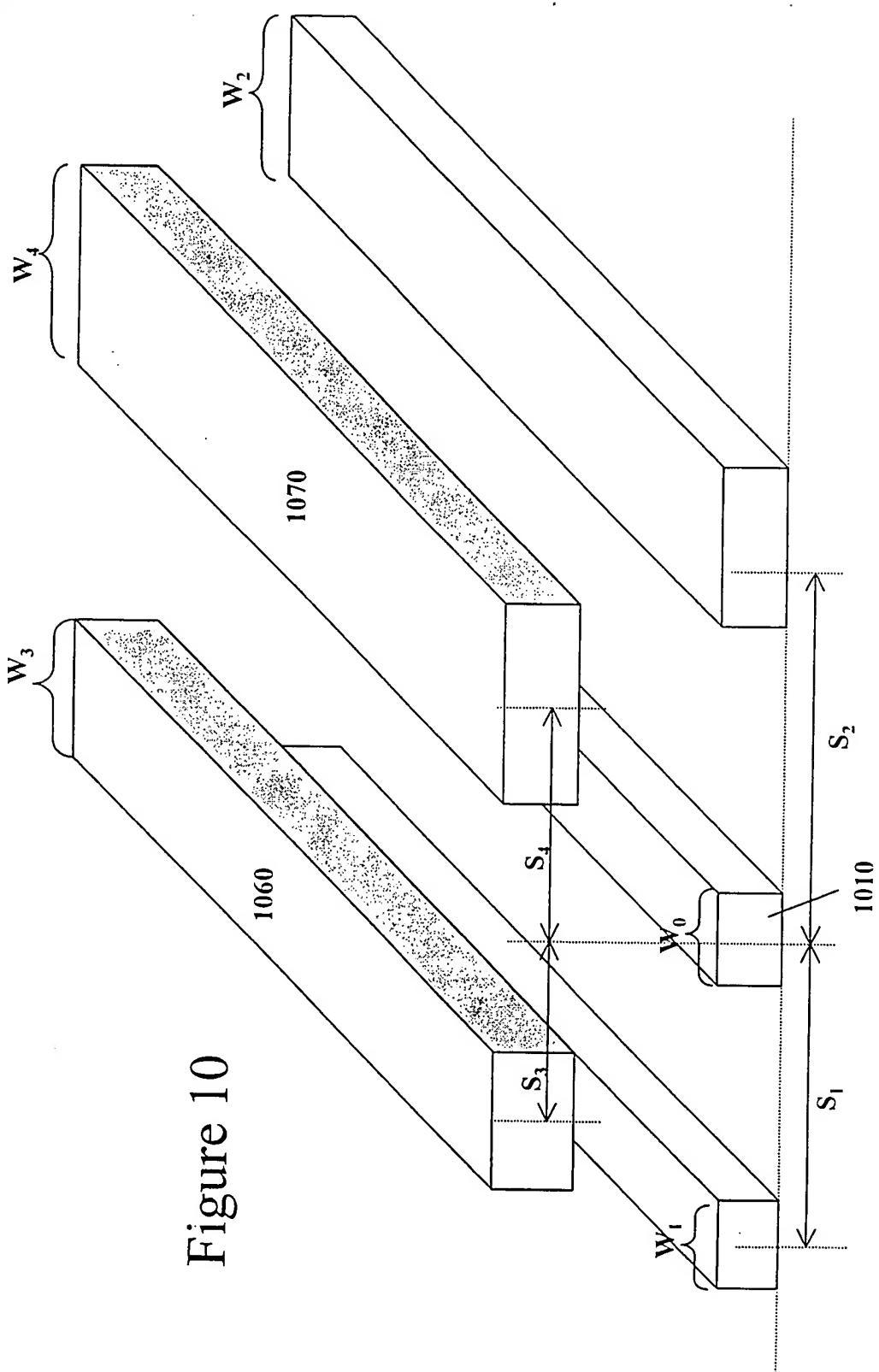
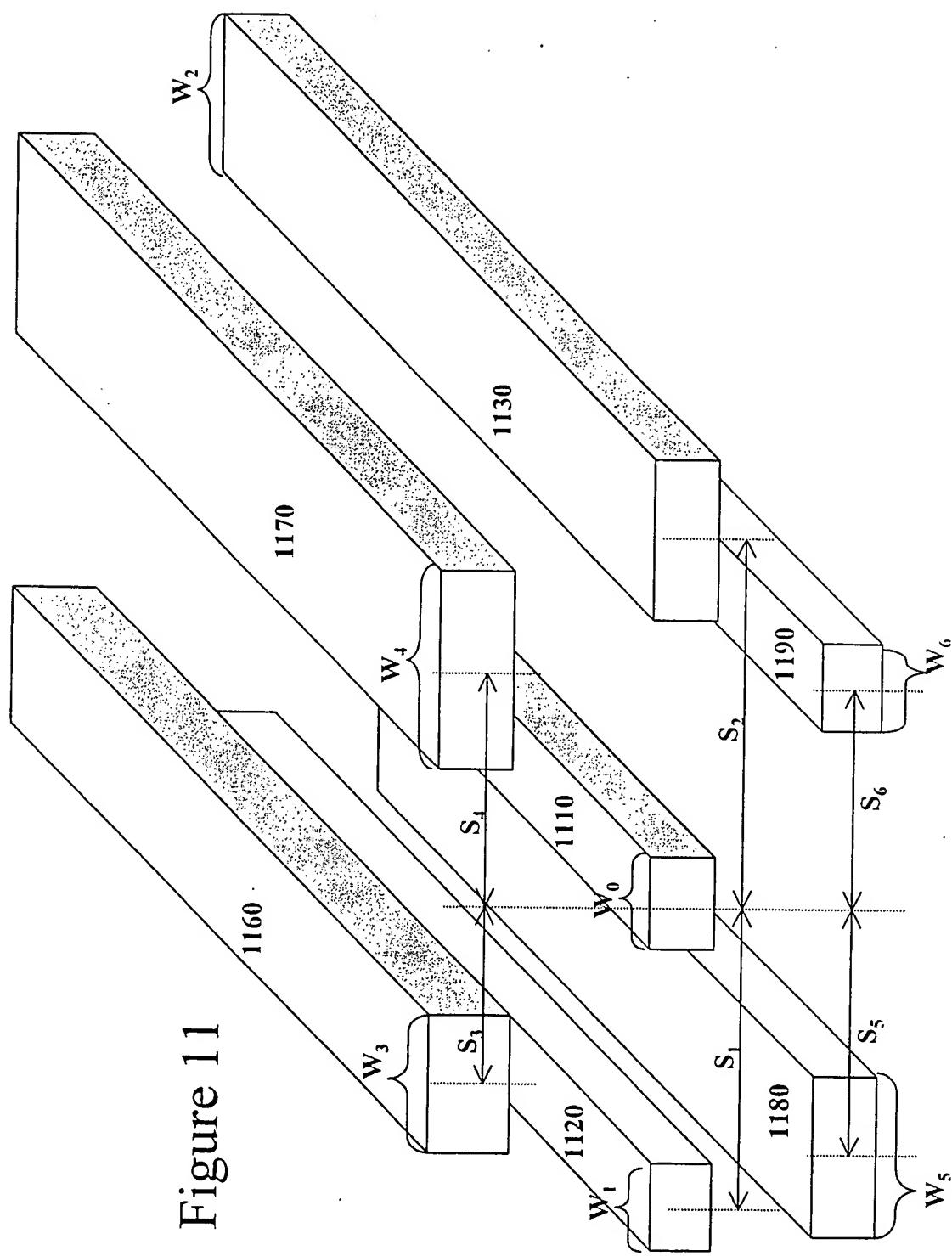


Figure 10



Figure 11



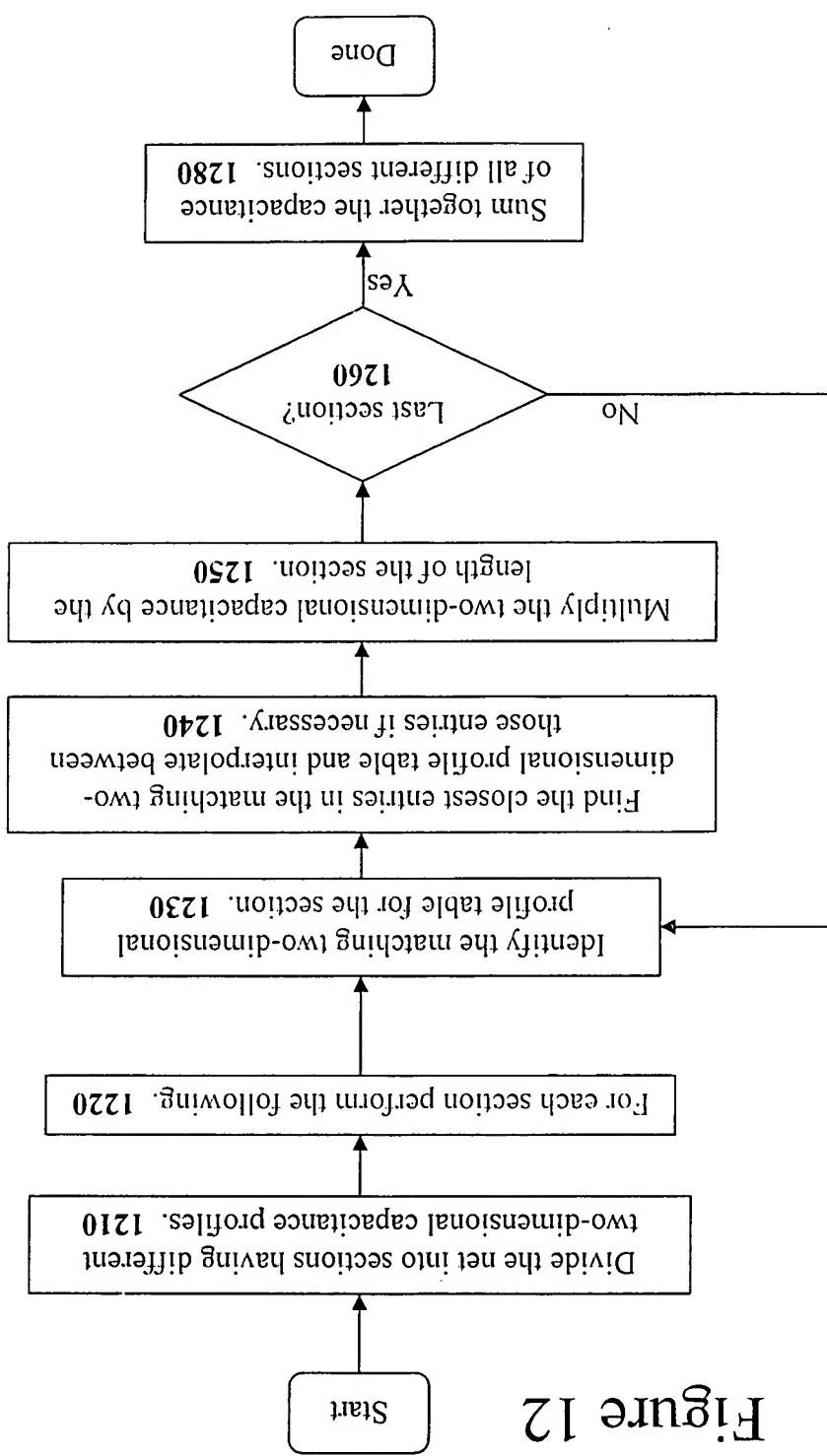


Figure 12

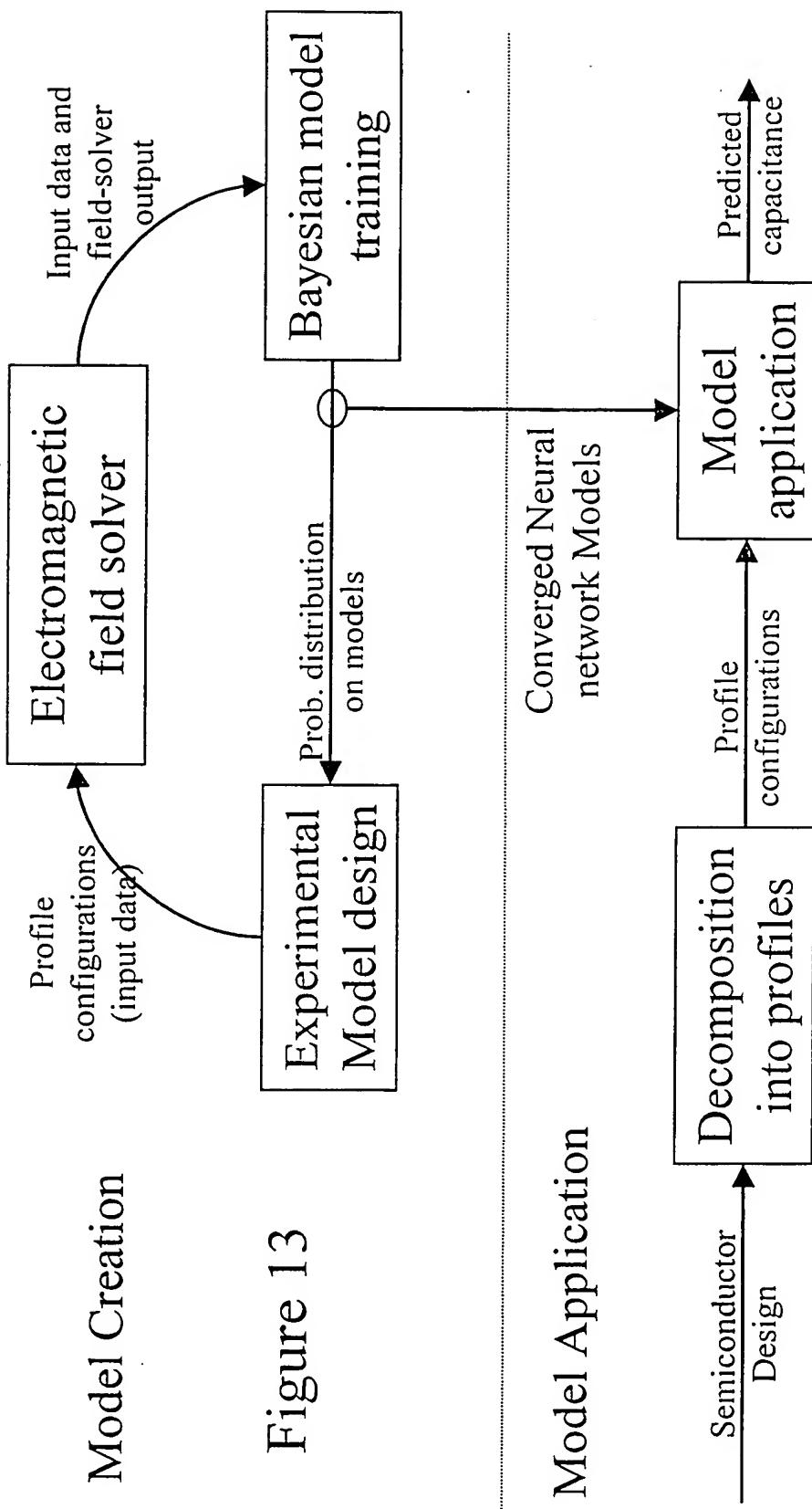




Figure 14

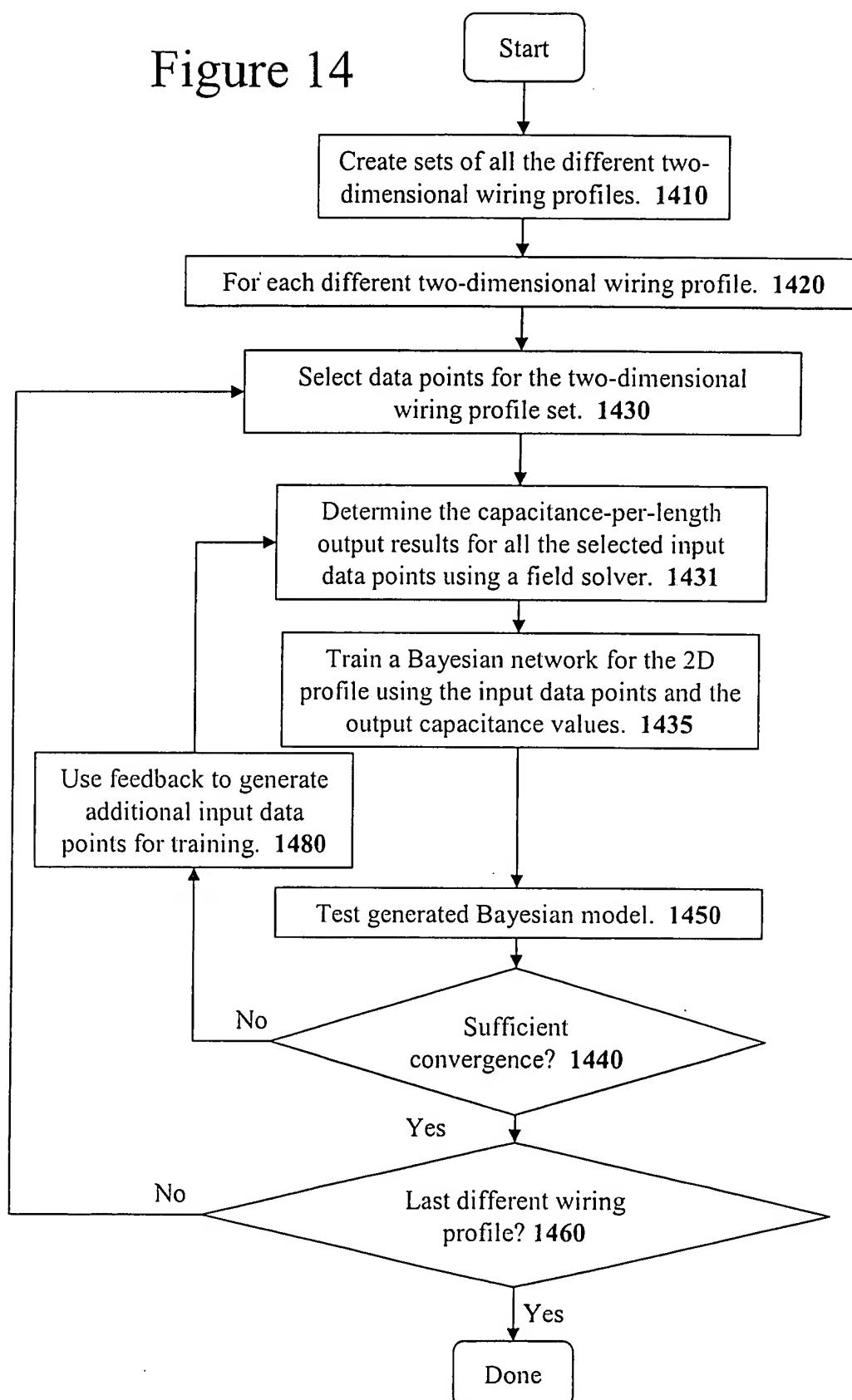
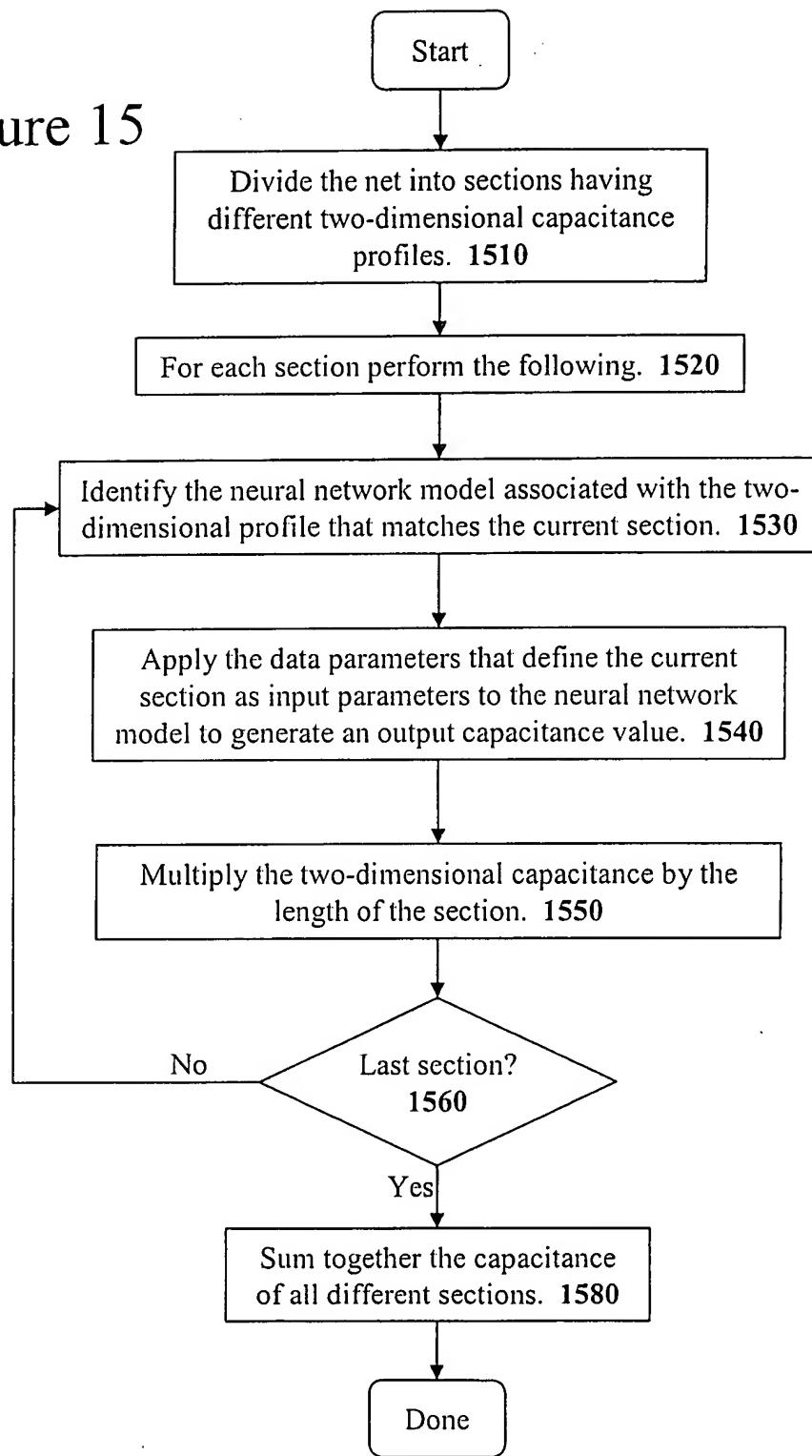




Figure 15



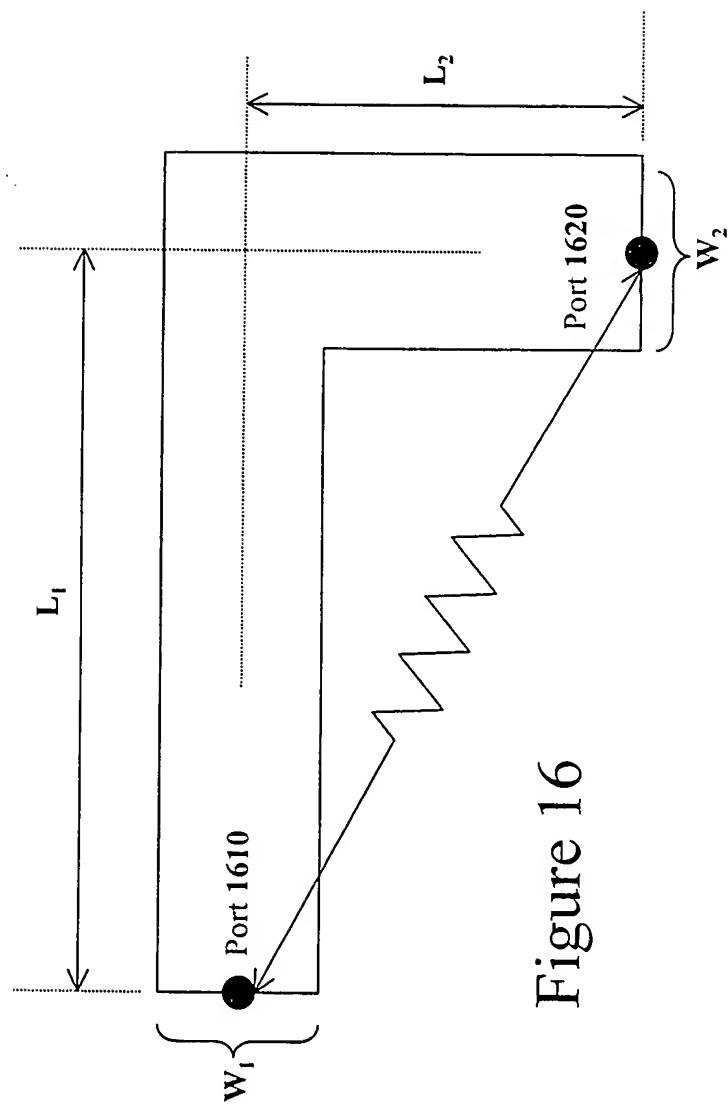


Figure 16

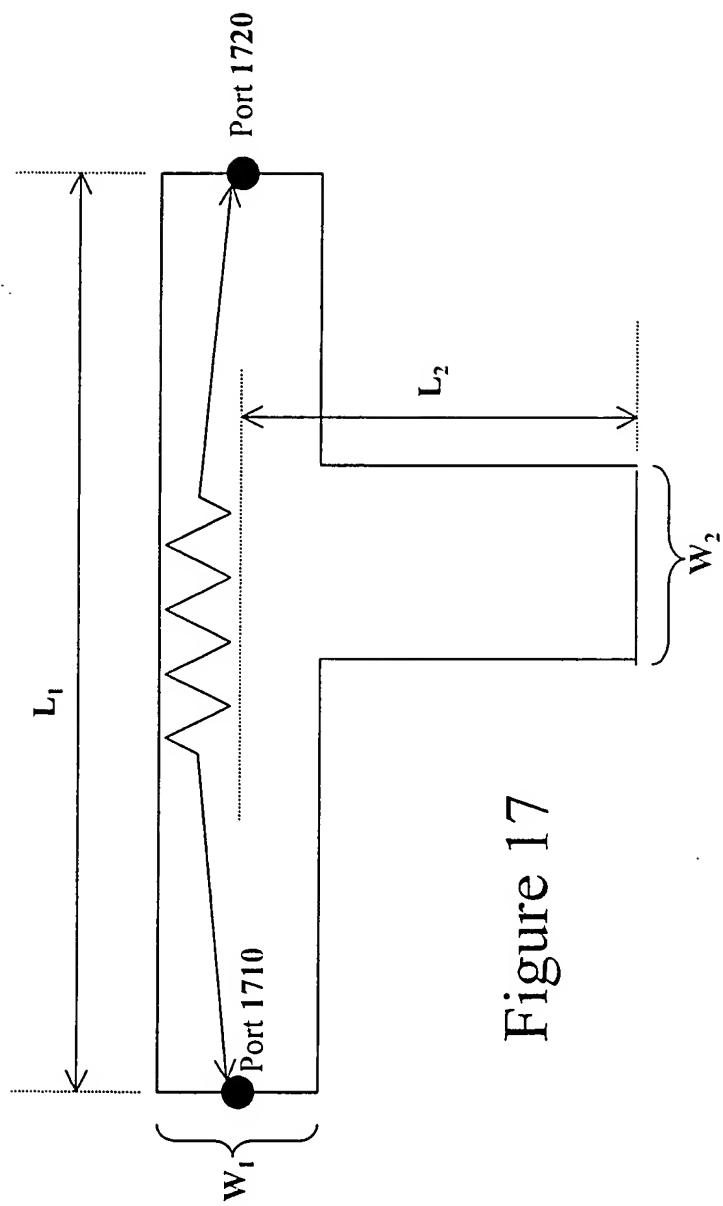


Figure 17

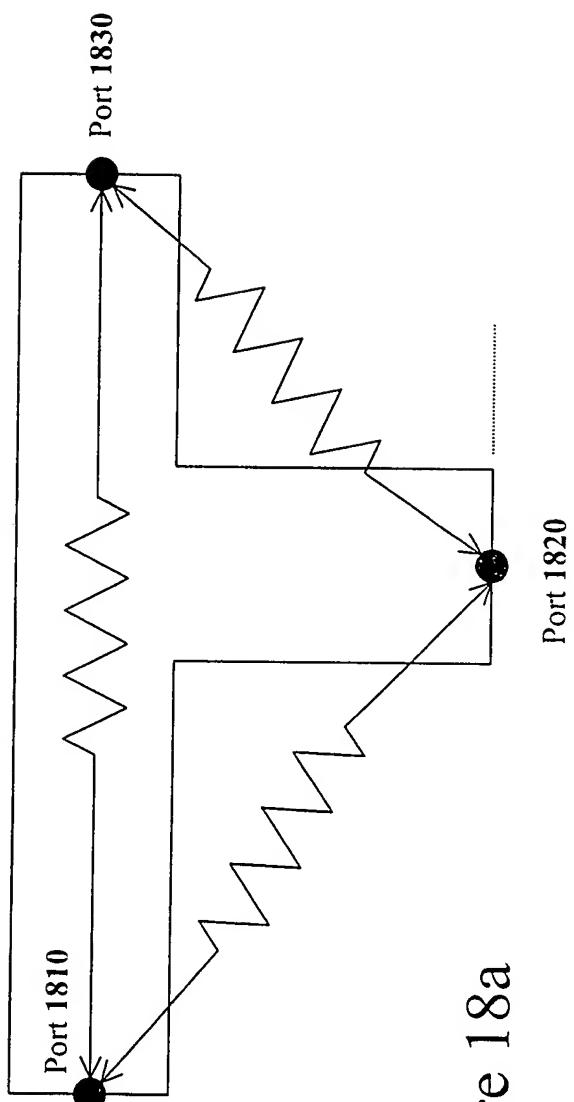


Figure 18a

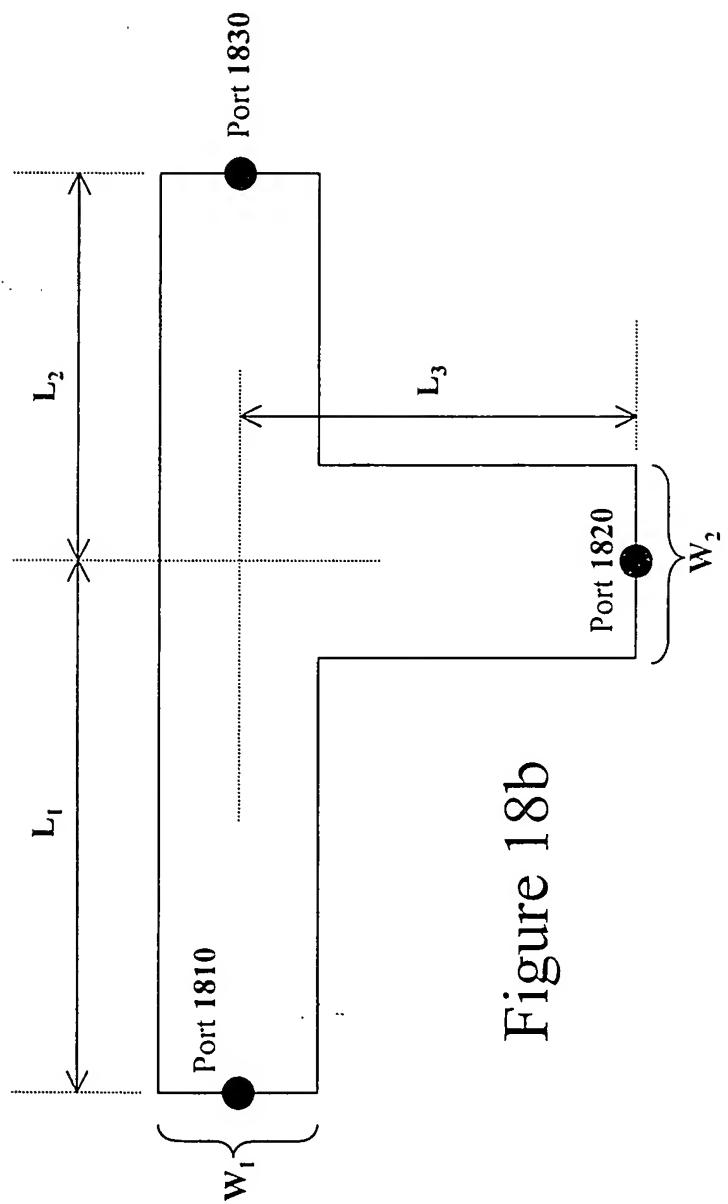


Figure 18b



Figure 19a

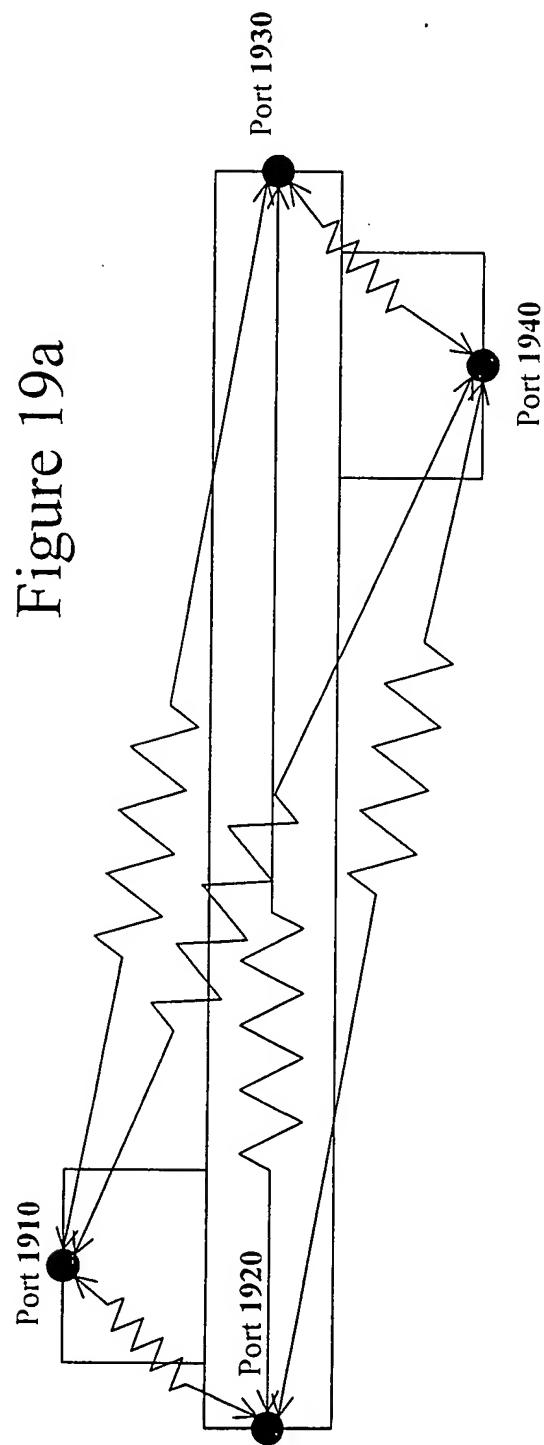




Figure 19b

